INTEGRATED CIRCUIT **TOSHIBA** TECHNICAL DATA

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT TA7784P

SILICON MONOLITHIC

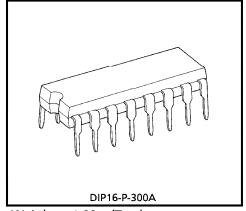
DUAL PREAMPLIFIER FOR AUTOREVERSE

The TA7784P is dual preamplifier for autoreverse type and W-cassette type tape player.

This IC contains dual preamplifier, forward/reverse control switches and metal/normal tape equalizer control switches.

FEATURES

- Built-in Forward / Reverse (TAPE1 / TAPE2) Control Switches.
- Built-in Metal/Normal Tape Equalizer Control Switches.
- High Voltage Gain
 - : $G_{VO} = 95dB$ (Typ.) ($V_{CC} = 6V$, f = 1kHz)
- **Operating Supply Voltage Range**
 - : $V_{CC(opr)} = 3.5 \sim 15V (Ta = 25^{\circ}C)$
- Input Coupling Capacitor Less
- Low Noise (Equivalent Input Noise Voltage)
 - : $V_{ni} = 1.0 \mu V_{rms}$ (Typ.) $(R_q = 600\Omega, BW = 20\sim 20kHz, NAB EQ)$

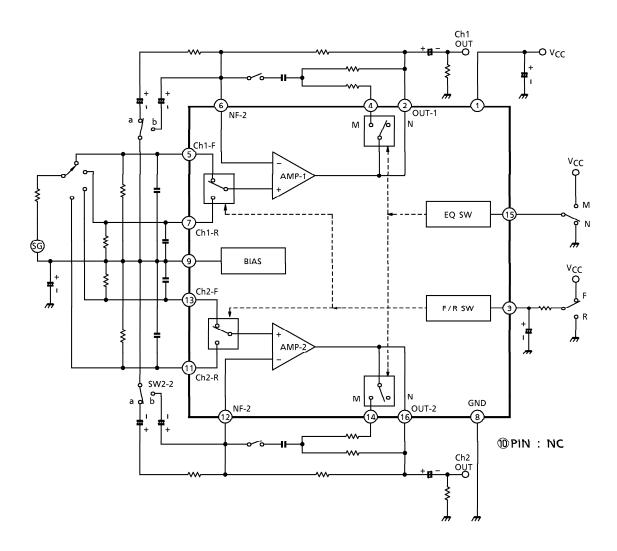


Weight: 1.00g (Typ.)

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BLOCK DIAGRAM



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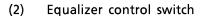
APPLICATION INFORMATION

- (1) Forward/reverse select switch
 - 1 Threshold voltage

Pin③ is coupled to the base of Q1 (PNP-Tr) as shown Fig.1. Threshold voltage

REVERSE	0~0.3V
FORWARD	1.1~V _{CC}

- ② The recommended forward/reverse select circuit is shown in Fig.2.
- ③ I_3 (In Fig.1) $I_3 = 12\mu A$ (Max.) (Ta = 25°C)



 $Pin \oplus is$ coupled to the base of Q2 (PNP-Tr) as shown Fig.3. The emitter potential of Q2 is 2.6Vdc.

Threshold voltage

METAL	2.1~V _{CC}
NORMAL	0~1.2V

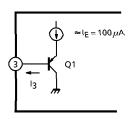


Fig.1

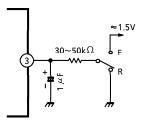


Fig.2

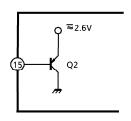


Fig.3

(3) C2~C5

Capacitor C2~C5 may be required for preventing a instability caused by the pattern layout or interference of external high frequency signal.

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MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	16	V
Power Dissipation	P _D (Note)	750	mW
Operating Temperature	T _{opr}	- 30∼75	°C
Storage Temperature	T _{stg}	- 55∼150	°C

(Note) Derated above $Ta = 25^{\circ}C$ in the proportion of $6mW/^{\circ}C$.

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, $V_{CC} = 6V$, f = 1kHz, $R_L = 10k\Omega$, $R_Q = 600\Omega$, Ta = 25°C, metal EQ)

(comess concentrate specime			,L,g,	,			
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Quiescent Current	^I CCQ (1)	_	V _{in} = 0, NORMAL EQ	_	5.5	_	mΑ
Quiescent Current	IccQ (2)	_	V _{in} = 0, METAL EQ	_	7.0	11	IIIA
Open Loop Voltage Gain	G _{vo}	_	$C_f = 100 \mu F, R_f = 0$	_	95	_	dB
Maximum Output Voltage	V _{om}	_	THD = 0.5%	1.1	1.5	_	V _{rms}
Total Harmonic Distortion	THD	_	V _{out} = 0.5V _{rms}	_	0.035	0.12	%
Equivalent Input Noise Voltage	V _{in}	_	$R_g = 620\Omega$, NAB BW = 20Hz~20kHz, Nor. EQ	_	1.0	1.7	μ V $_{rms}$
Ripple Rejection	R.R.	_	$f_{ripple} = 100Hz, V_{in} = 1V_{rms}$	_	55	_	dB
Cross Talk	C.T.	_	$V_{out} = 0.775V_{rms}$ (0dBm)	50	60	_	dB
Forward / Reverse Cross Talk	C.T. (F/R)	_	V _{out} = 0.775V _{rms} (0dBm)	60	70	_	dB

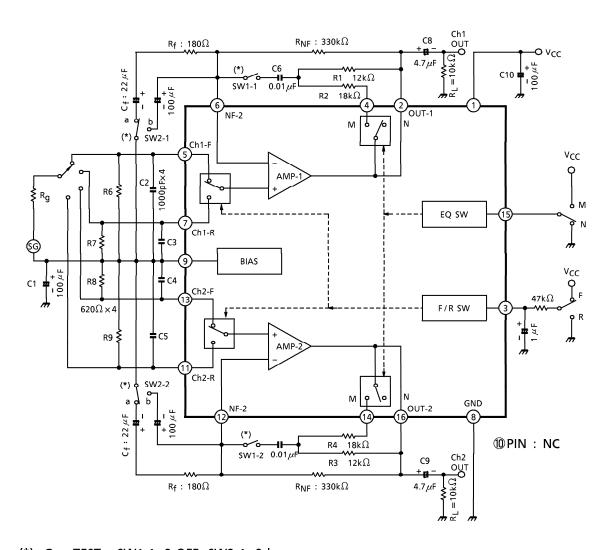
TYPICAL DC VOLTAGE OF EACH TERMINAL ($V_{CC} = 6V$, Ta = 25°C, dual mode test circuit)

TERMINAL No.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DC- VOLTAGE (V)	Vcc	2.3	V _{CC} GND	2.2	2.2	2.2	2.2	GND	2.2	NC	2.2	2.2	2.2	2.2	VC/ GND	2.2

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TEST CIRCUIT

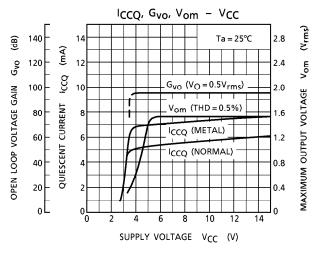


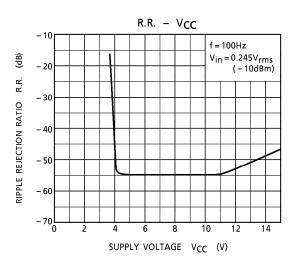
(*) G_{VO} TEST : SW1-1, 2-OFF, SW2-1, 2-b

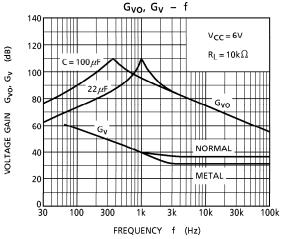
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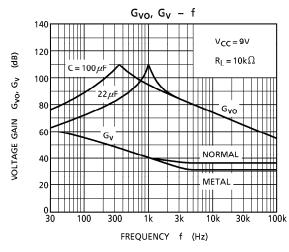
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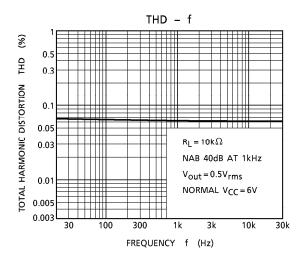
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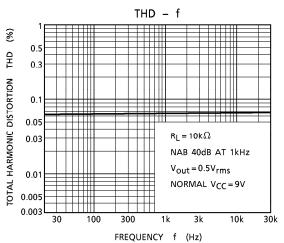








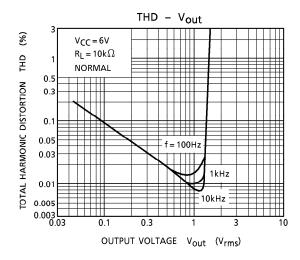


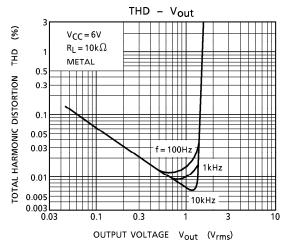


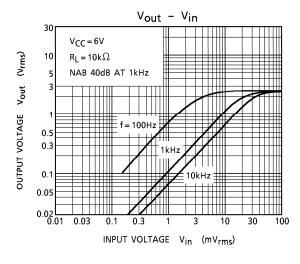
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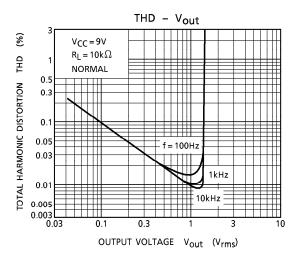
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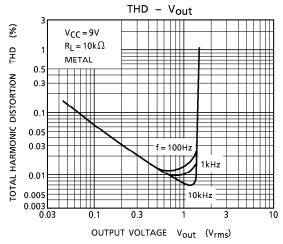
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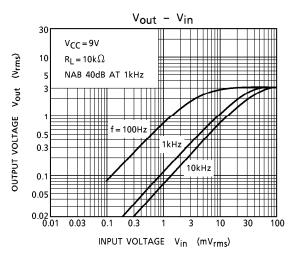








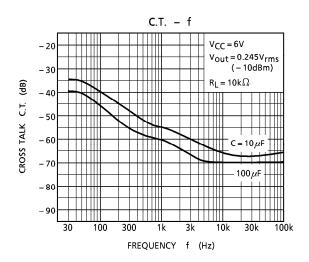


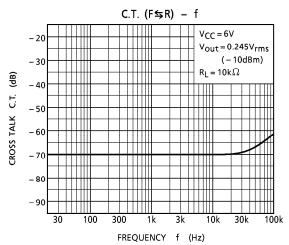


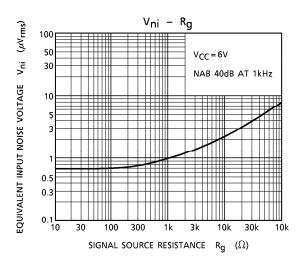
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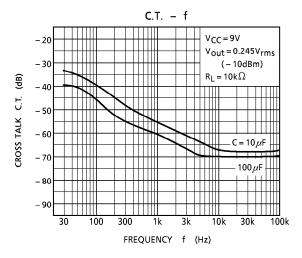
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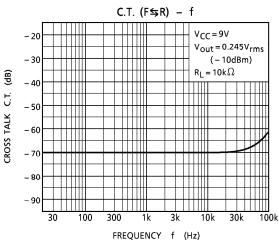
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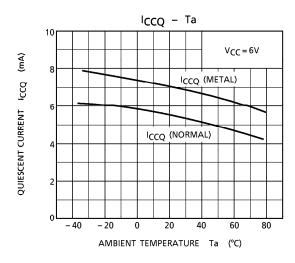








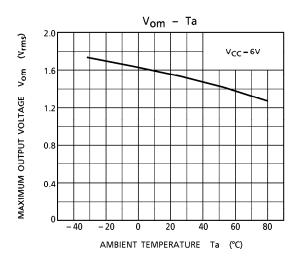


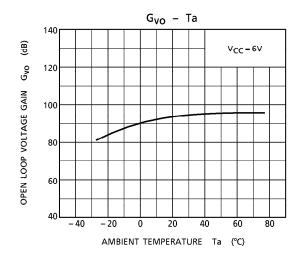


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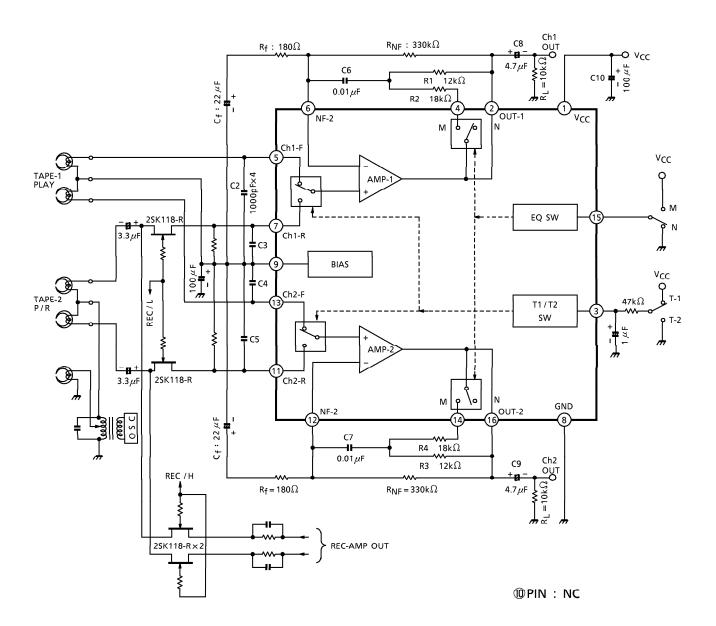
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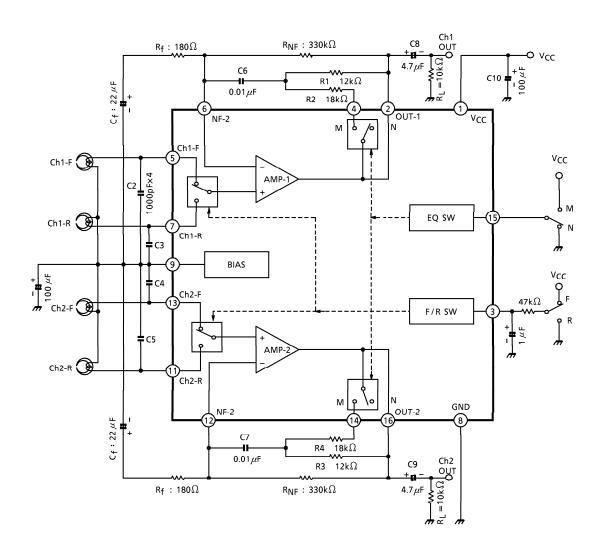
APPLICATION 1 (Double cassette player)



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APPLICATION 2 (Autoreverse)

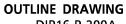


10 PIN : NC

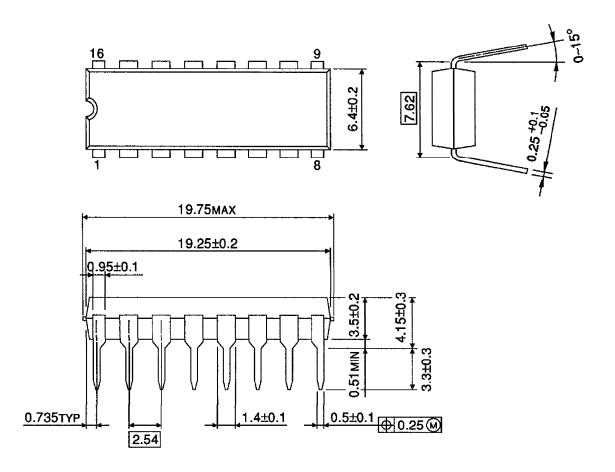
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DIP16-P-300A Unit: mm



Weight: 1.00g (Typ.)

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